Khaled M. Elleithy, Ph.D.

Dr. Elleithy received the B.Sc. degree in computer science and automatic control from Alexandria University in 1983, the MS Degree in computer networks from the same university in 1986, and the MS and Ph.D. degrees in computer science from The Center for Advanced Computer Studies at the University of Louisiana at Lafayette in 1988 and 1990, respectively. From 1983 to 1986, he was with the Computer Science Department, Alexandria University, Egypt, as a lecturer. From September 1990 to May 1995 he worked as an assistant professor at the Department of Computer Engineering, King Fahd University of Petroleum and Minerals, Dhahran, Saudi Arabia. From May 1995 to December 2000, he has worked as an Associate Professor in the same department. In January 2000, Dr. Elleithy has joined the Department of Computer Science and Engineering in the University of Bridgeport as an associate professor. In May 2003 Dr. Elleithy was promoted to full professor. In March 2006, Professor Elleithy was appointed Associate Dean for Graduate Programs in the School of Engineering at the University of Bridgeport.

Dr. Elleithy published more than two hundreds research papers in international journals and conferences. He has research interests are in the areas of computer networks, network security, mobile communications, and formal approaches for design and verification.

Dr. Elleithy has a distinguished record as a university professor in terms of teaching, research, and services.

1. **Teaching**

   Prof. Elleithy has more than 20 years of teaching experience (see CV for complete details). His teaching evaluations were distinguished in all the universities he joined. He supervised hundreds of senior projects as well as MS theses. He developed and introduced many new undergraduate/graduate courses. He also developed new teaching / research laboratories in his area of expertise.

2. **Research**

   Prof. Elleithy published more than 200 papers in quality international journals and conferences. Also, he is the editor or co-editor for 12 books by Springer. He is a member of technical program committees of many international conferences as recognition of his research qualifications. He served as a guest editor for several International Journals. He was the chairman for the International Conference on Industrial Electronics, Technology & Automation, IETA 2001, 19-21 December 2001, Cairo – Egypt. Also, he is the General Chair of the 2005, 2006, 2007, 2008, 2009, 2010, and 20111 International Joint Conferences on Computer, Information, and Systems Sciences, and Engineering virtual confernces.

3. **Administration**

   Dr. Elleithy serves as the Associate Dean for Graduate Studies in the School of Engineering since 2006. Dr. Elleithy was a chairman and a member of numerous departmental, college, and university committees in the past twenty years.

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Khaled M. Elleithy, Ph.D.

Mailing address

Computer Sc. & Eng. Department
University of Bridgeport
Tech Building room 229
221 University Avenue
Bridgeport, CT 06601

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Cell: (203) 260-9077
Fax: (203) 576 4766
Email: elleithy@bridgeport.edu
Page: http://KhaledElleithy.org

Education

- Ph.D., Computer Science, the Center for Advanced Computer Studies (CACS), University of Louisiana at Lafayette, USA, May 1990.
- M.S., Computer Science, the Center for Advanced Computer Studies (CACS), University of Louisiana at Lafayette, USA, May 1988.
- M.S., Computer Science and Automatic Control, Computer Science and Automatic Control Dept., Faculty of Engineering, University of Alexandria, Egypt, July 1986.
- B.S., Computer Science and Automatic Control, Computer Science and Automatic Control Dept., Faculty of Engineering, University of Alexandria, Egypt, May 1983.

Ph.D. Thesis

"*A Formal Framework for High Level Synthesis of Digital Designs*," M. A. Bayoumi (Chairman), The Center for Advanced Computer Studies, University of southwestern Louisiana, May 1990.

MSc Thesis

"*Multi-access Protocols in Local Area Networks,"* M. S. Selim (Chairman), Computer Science and Automatic Control, Alexandria University, Alexandria, Egypt, July 1986.

Research Interests

Computer Networks, network security, and wireless communications
Formal approaches for design and verification
Computer Architecture
Computer Arithmetic

Current Positions

Associate Dean for Graduate Programs, School of Engineering, University of Bridgeport, *Bridgeport, Connecticut, USA*

Professor, Computer Science and Engineering Department, University of Bridgeport, *Bridgeport, Connecticut, USA*

Professor, Electrical Engineering Department, University of Bridgeport, *Bridgeport, Connecticut, USA*

Director, Mobile and Wireless Communications Laboratory, University of Bridgeport, *Bridgeport, Connecticut, USA*

PH.D. Program Coordinator, University of Bridgeport, *Bridgeport, Connecticut, USA*

MSC Online program Advisor, of Bridgeport, *Bridgeport, Connecticut, USA*

Employment History

Associate Dean for Graduate Programs, School of Engineering, University of Bridgeport, *Bridgeport, Connecticut, USA* (March 2006 – now)

Professor, *Computer Science and Engineering Department, University of Bridgeport, Bridgeport, Connecticut, USA* (May 2003 - Now)

Digital logic	(CpE 210)	sophomore
Embedded System Design	(CpE 387)	Junior
Data Communications/Networks	(CpE 471)	Senior/Graduate
Database Design	(CS 450)	Senior/Graduate
Performance Evaluation of Systems	(CpE/CS 560)	Graduate
Network Security	(CpE/CS 561)	Graduate
Mobile Communications	(CPE481)	Senior/Graduate
Network Administration	(CPE 482)	Senior/Graduate
Wireless Sensor Networks	(CPSE 691)	Graduate
Quantum Computing	(CPEG 606)	Graduate

Adjunct Instructor, Computer Science Department, Sacred Heart University, Fairfield, Connecticut.

Network Security	(CS 622)	Graduate
Advanced Network Security	(CS 623)	Graduate

Online Instructor, Graduate InfoSec Program, Computer Science department, James Madison, MSC 4103, Harrisonburg, VA 22807 (Summer 2002, 2003)

Associate Professor, *Computer Science and Engineering Department, University of Bridgeport, Bridgeport, Connecticut, USA* (January 2000 – May 2003)

Associate Professor, *Computer Engineering Department, King Fahd University of Petroleum and Minerals,* Dhahran, Saudi Arabia. (April 95-Dec 99)

Assistant Professor, *Computer Engineering Department, King Fahd University of Petroleum and Minerals,* Dhahran, Saudi Arabia. (Sep90-April 95)

<u>Undergraduate</u>

Logic Design I	Sophomore

Computer Architecture I	Junior
Logic Design II	Junior
Data Communications for Business	Junior
Data Communications	Junior
Personal Computers	Senior
Computer Architecture II	Senior
Computer Networks	Senior
Bit slice microprocessor Design	Senior
VLSI RISC Architectures	Senior

Graduate
Architecture and Design of Computer Systems
Advanced Computer Architecture
Design of Arithmetic Units
Foundations of Hardware Design Verification.
Performance Evaluation of Computer Systems

Research Associate, *The Center for Advanced Computer Studies, University of South Western Louisiana,* Lafayette, USA. (May90-Sep90)
Research Assistant, *The Center for Advanced Computer Studies, University of South Western Louisiana,* Lafayette, USA. (May88-May90)
Teaching Assistant, *Electrical and Computer Engineering Department,* University of South Western Louisiana, Lafayette, LA, USA. Different Dept. Courses (Lab instructing, Grading) -20 hours/week- (Aug86-May88)
Tutor, *The International Student Office, University of South Western Louisiana,* Lafayette, USA. (Aug86-May90)
System Programmer, *Gathwary computers,* Alexandria, Egypt. Participation in the design and implementation of different applications. Aug85-Aug86)
Lecturer, *Air Defense College,* Alex., Egypt. Digital systems and microprocessor courses and labs. -10 hours/week- (Apr85-Aug86)
System Programmer, *Air Defense College,* Alexandria, Egypt. Participation in the design and implementation of different data-base applications in the period (Jan84-Apr85)
Lecturer, *Computer Sc. Dept., Alex. University,* Egypt. Preparing, consulting, grading, and lab instructing for different department courses. -14 hours/week- (Oct83-Aug86)
Lecturer, *Arab Maritime Transport Academy,* Alex., Egypt. Programming Languages courses and Digital lab. -10 hours/week- (Aug83-May85)
Programmer, *Computer Center, Achen University,* Achen, West Germany. Design of a package for simulating time sharing systems (July 82-Sep82)
Programmer, *Computer Sc. Dept., Faculty of Eng., Alex. University,* Egypt. Participation in the design and implementation of many packages (Registration, planes reservation,). (July 80-Sep 80), (July 81-Sep81)

Research Grants

- Co-Investigator, Advanced Technology in Support of Hybrid Projectiles, FY 2010 Defense Appropriation Request, $2.4 million; 1/1/2010 - 7/1/2012.
- Co-Investigator, Hybrid Projectile Vision System, Imperial Machine and Tool Company, $30,000; April 2010 - July 2010.
- Co-Investigator, Development of a travel grant, U.S. Army Armanent, Research, Development & Engineering Center (ARDEC), $5,000, Funded. [1/2010]
- Co-Investigator, Hybrid Projectile Design," U.S. Army Armament, Research, Development & Engineering Center (ARDEC), $57,688, Funded. [8/1/2008 - 5/31/2009]

- Principal Investigator, "Implementation and Performance Evaluation of Overlay End System Multicast (ESM) for stable and Fast Streaming of Multimedia Applications," University of Bridgeport Seed Money Grants, $10,000, Fall 2009, approved for funding.
- Prinicipal Investigator, "Wireless LAN Laboratory," NSF, June 1, 2004, through May 31, 2005; $100,000 plus $100,000 in University of Bridgeport matching funds, Pending.
- Prinicipal Investigator, "Electronic Assesment of Undergraduate Porgrams (E-Assesment)" NSF, June 1, 2004, through May 31, 2006; $520,000, Pending.
- Co-PI, "Refining and Improving Science and Mathematics Understanding through Partnerships (Ris'm Up)", NSF, July 1, 2004 through June 30, 2009, $7,000,000, Pending.
- Prinicipal Investigator, "Mobile Content Mnanger," United Nations Develoment Program (UNDP), Transfer of Knowledge through Expatriate Nationals (TOTKEN) Fund, $3,500, June 2001, Funded.
- Co-Investigator, "Developing an Intelligence Web Based Expert System for Anemia Diagnosis and Categorization," Joint Science and Technology Fund, U.S. Egypt Joint Board on Scientific and Technological Cooperation, $50,000, October 2001, Senior Investigator, Pending.
- Co-Investigator, "Security Implementation and Evaluation of Armco Enterprise Network," Saudi Armco, SR 200,000, May 1999- May 2000, Funded.
- Principle Investigator, *"A Formal Methodology for Parallel VLSI Algorithm Design,"* Jan. 1993-Dec. 1995. King Abdul Aziz City of Science and Technology (KACST), SR 293,000, Funded.

Ph.D. Dissertation Supervision (Partial List)

- Ali Elrashedi, A new Analytical Performance Model for Cylindrical Microstrip Printed Antenna, August 2012, *Advisor*.
- Mohammed A. Abuhelaleh, SOOAWSN: A secure object oriented architecture for wireless sensor networks, May 2011, *Advisor.*
- Syed Rizvi, A Generic Optimized Time Management Algorithms (OTMA) Framework for Simulating Large-Scale Overlay Networks, December 2010, *Advisor.*

MS Thesis Supervision (Partial List)

- Topological Design of Enterprise Networks
- A Hardware Model of An expandable RSA Cryptographic System
- Evaluation and Comparison of Internet Firewalls
- Designing a Self-Timed ALU
- Organization of Parallel Memories
- Design of a Formal Specification Language for Parallel VLSI Algorithms
- Design and Modeling of a Real Time RISC Processor
- Backend Design of a Formal High Level Synthesis System
- A BIST for a Histogrammer Memory Chip

Senior Project Supervision (Partial list)

- Design of Residue Number System Arithmetic Processors.
- Performance Study of High Speed Adders
- Implementation of an Optimal Algorithm for RNS Arithmetic Processor Design
- Performance Study of RISC Processors
- Formal Hardware Verification
- A MIPS-X RISC Processor Emulator.
- Parallelizing Sorting Algorithms using OCCAM and Transputer
- An RNS Processor Emulator using Occam and Transputer.
- Hardware Verification Using an Expert System Shell

- Interfacing D/A and D/A Converters to a Transputer Based Machine
- A Transputer Based Sonar Range Finding
- Implementation of a new data link layer Protocol
- Analysis and study of Routing Protocols
- Design and implementation of Academic department database
- Translation of Web page to Arabic language

Professional Societies

- Member of the Egyptian Engineering Syndicate since June 1983
- Member of the honor society of Phi Kappa Phi University of South Western Louisiana Chapter since April 1989.
- Member of the IEEE computer society since 1988.
- Member of circuits & systems society since 1988.
- Member of ACM SIGARCH (Special Interest Group on Computer Architecture) since 1990.
- Member of the Association of Computing Machinery (ACM) since 1990.

Committees Membership

Dr. Elleithy has been a chairman and a member in numerous departmental, college, university committees in the past fifteen years.

Professional Activities

Conferences Chairman

- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 12), December 2012, Virtual Conference.
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 11), December 2011, Virtual Conference.
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 10), December 2010, Virtual Conference.
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 09), December 2009, Virtual Conference.
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 08), December 2008, Virtual Conference.
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 07), December 2007, Virtual Conference.
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 06), December 2006, Virtual Conference.
- Chairman, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 05), December 2005, Virtual Conference.
- Chairman, International Conference on Industrial Electronics, Technology & Automation, IETA 2001, 19-21 December 2001, Cairo – Egypt.

Membership in Technical Committees

1. Member, 2010 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2010.
2. Member, 2009 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2009.
3. Member, 2008 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2008.

4. Member, 2007 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2007.
5. Member, 2006 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2006.
6. Member, Program Committee of World Multiconference on Systemics, Cybernetics and Informatics (SCI 2006) Orlando, USA, July, 2006.
7. Member, 2005 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2005.
8. Member, Program Committee of World Multiconference on Systemics, Cybernetics and Informatics (SCI 2005) Orlando, USA, July, 2005.
9. Member, 2004 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2004.
10. Member, 2004 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2004.Member, World Multiconference on Systemics, Cybernetics and Informatics (SCI 2004) Orlando, USA, July, 2004.
11. Member, 2003 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Canada, July, 2003.
12. Member, 2003 International Program Committee of the 15th International Conference on Parallel and Distributed Computing and Systems in Marina del Rey, CA, USA, November 3-5, 2003.
13. Member, 2003 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Calgery, Canada, July, 2003.
14. Member, 7th World Multiconference on Systemics, Cybernetics and Informatics (SCI 2003) Orlando, USA, July, 2003.
15. Member, 2002 International Program Committee of the 14th International Conference on Parallel and Distributed Computing and Systems in Cambridge (USA), November 4-6, 2002.
16. Member, 6th World Multiconference on Systemics, Cybernetics and Informatics (SCI 2002) Orlando, USA, July, 2002.
17. Member, 2002 Program Committee of Workshop on System-on-Chip for Real-Time Applications, Banff, Canada, July 6-7, 2002
18. Member, International Program Committee of the 13th International Conference on Parallel and Distributed Computing and Systems (PDCS'01) in California (USA), August 21 - 24, 2001
19. Member, Technical committee of the 2001, Applied Informatics (AI) 2001, February 19-22, 2001 Innsbruck, Austria
20. Member, Program Technical Committee of the 2001 IEEE International Conference on Robotics and Automation in Seoul, Korea
21. Member, Program Technical Committee of the 2000 IEEE Workshop on SiGNAL PROCESSING SYSTEMS (SiPS) Design and Implementation, October 11-13, 2000, Lafayette, Louisiana.
22. Member, Program Technical Committee of 8th Great Lake Symposium on VLSI, GLS'98, Feb. 1998, Lafayette, Louisiana.
23. Member, Program Technical Committee of Conference on Parallel and Distributed Computing and Systems, PDCS 97, June 1997, Barcelona.
24. Member, Program Technical Committee of Eighth IASTED International Conference on Parallel and Distributed Conference, Oct. 1996, Chicago.
25. Member, Program Technical Committee of seventh International Conference on Microelectronics, Dec 1995, Malaysia.
26. Member, Program Technical Committee of 37th Midwest Symposium on Circuits and Systems, August 3-5, Lafayette, LA, USA, 1994.
27. Member, Program Technical Committee of sixth International Conference on Microelectronics, September 5 - 7, Istanbul, Turkey, 1994.
28. Member, Program Technical Committee of fifth International Conference on Microelectronics, Dec. 13-16, 1993, Dhahran, Saudi Arabia.

Membership in Standing Committees

Member, IASTED Standing Technical Committee on Parallel and Distributed Computing and Systems. The committee is responsible for the planning and organization of IASTED activities such as meetings and publications.

Member, American Biographical Institute Research Board of Advisors. The American Biographical Institute publishes the following biographical reference works since 1967:

The International Directory of Distinguished Leadership
The International Who's Who of Professional and Business Women
The Dictionary of Leading Americans and the World Who's Who of Women

<u>*Editorial*</u>

- Editor, Special issue on Wireless LANs, Journal of Internet Technology, April 2003.
- Editor, Special issue on Web-based controlled devices, International Journal of Robotics and Automation, November 2002.
- Editor, Special issue on network security, Journal of Internet Technology, April 2002.

Published Work

Books

1. Khaled Elleithy and Tarek Sobh, *Innovations and Advances in Computer, Information, Systems Sciences, and Engineering*, Springer, September 2012.
2. Tarek Sobh and Khaled Elleithy, *Emerging Trends in Computing, Informatics, Systems Sciences, and Engineering*, Springer , Augsut 2012.
3. Tarek Sobh and Khaled Elleithy, *Innovations in Computing Sciences and Software Engineering*, Springer, November 2010.
4. Khaled Elleithy, Tarek Sobh, Magued Iskander, V Kapila, M.A. Karim, and Ausif Mahmood, *Technological Developments in Networking, Education and Automation*, July 2010.
5. Tarek Sobh, Khaled Elleithy, Ausif Mahmood, *Novel Algorithms and Techniques in Telecommunications and Networking*, Springer, March 2010.
6. Khaled Elleithy, *Advanced Techniques in Computing Sciences and Software Engineering*, Springer, March 2010.
7. Khaled Elleithy, *Innovations in Systems, Computing Sciences and Software Engineering*, Springer, August 2008.
8. Tarek Sobh, Khaled Elleithy, Ausif Mahmood and Mohamed Karim, *Novel Algorithms and Techniques in Telecommunications, Automation and Industrial Electronics*, Springer, August 2008.
9. Khaled Elleithy, *Advances and Innovations in Systems, Computing Sciences and Software Engineering*, Springer, June 2007.
10. Tarek Sobh, Khaled Elleithy, Ausif Mahmood and Mohamed Karim, *Innovative Algorithms and Techniques in Automation, Industrial Electronics and Telecommunications*, Springer, June 2007.
11. Khaled Elleithy, Tarek Sobh, Ausif Mahmood, Magued Iskander and Mohammad Karim, *Advances in Computer, Information, and Systems Sciences, and Engineering*, Springer, September 2006.
12. Tarek Sobh and Khaled Elleithy A*dvances in Systems, Computing Sciences and Software Engineering: Proceedings of SCSS 2005*, Springer, September 2006.

Book Chapters

1. Abdul Razaque and Khaled Elleithy,"Detection of Attacks for Restoring Privacy of Users to Improve Mobile Collaborative Learning (MCL) Over Heterogeneous Network," *Communications in Computer and Information Science*, Springer, Volume 294, 2012, pp 201-216.
2. Abdul Razaque and Khaled Elleithy," Ambient Intelligence Paradigm to support pedagogical activities over wireless sensor networks," *Ubiquitous Computing and Ambient Intelligence (UCAmI)*, Springer, 2012.
3. Abdul Razaque, Aziz Alotaibi, Khaled Elleithy "Location Based Overlapped Mobility Aware network Model," *Innovations and Advances in Computer, Information, Systems Sciences, and Engineering*, Springer, September 2012.

4. Syed S. Rizvi, Khaled Elleithy, and Aasia Riasat, "The Use of Orthogonal Frequency Code Division (OFCD) Multiplexing in Wireless Mesh Network (WMN*)," Fourth-Generation (4G) Wireless Networks: Applications and Innovations*, pp. 565-581, 2010.
5. Syed S. Rizvi, Majid A. Jafri, and Khaled Elleithy, "A Novel Optimization of the Distance Source Routing (DSR) Protocol for the Mobile Ad Hoc Networks (MANET)," *Novel Algorithms and Techniques in Telecommunications and Networking*, Springer, pp. 269 – 274, 2010.
6. Aasia Riasat, Syed S. Rizvi, Khaled M. Elleithy, Farheen Zehara, and Faraz Arain, "The Role of System Dynamics in Learning Environments," *Advances in Automation, Industrial Electronics and Telecommunications*, Springer, June 2008
7. Syed S. Rizvi, Khaled M. Elleithy, Khushboo Patel, and Chaitali Patel, "Use of Self-Adaptive Methodology in Wireless Sensor Networks for Reducing the Energy Consumption", *Advances in in Automation, Industrial Electronics and Telecommunications*, Springer, June 2008
8. Syed S. Rizvi, Aasia Riasat, Muhammad S. Rashid, and Khaled M. Elleithy, "Bandwidth Problem in High Performance Packet Switching Network," *Innovations in Systems, Computing Sciences and Software Engineering*, Springer, June 2008.
9. Ajay Shrestha, Khaled M. Elleithy, and Syed S. Rizvi, "Investigating the effects of Encoder Schemes, WFQ & SAD on VoIP QoS," *Advances in Automation, Industrial Electronics and Telecommunications*, Springer, June 2008.
10. Aasia Riasat, Syed S. Rizvi, Khaled M. Elleithy, Faraz Arain, Rizwan M. Qureshi, "Dynamic Semantics of the Web: Useful Toll for the New Generation Agent-Based Software," *Advances in Automation, Industrial Electronics and Telecommunications*, Springer, June 2008.
11. Khurram M. Rajput, Khaled M. Elleithy, and Syed S. Rizvi, "A Novel Approach for Creating Consistent Trust and Cooperation (CTC) among Mobile Nodes of Ad Hoc Network," *Advances in Automation, Industrial Electronics and Telecommunications*, Springer, June 2008.
12. Syed S. Rizvi, K. M. Elleithy, Aasia Riasat, "Minimizing the Null Message Exchange in Conservative Distributed Simulation," Book Chapter, Innovative Algorithms and Techniques in Automation, Industrial Electronics and Telecommunications, Springer, June 2007.
13. K. M. Elleithy and I. Rimawi, "Design, Analysis and Implementation of a Cyber Vote System." Book Chapter, *Advances in Computer, Information, and Systems Sciences, and Engineering: Proceedings of IETA 2005, TeNe 2005 and EIAE 2005*, Springer, September 2006.

Journal Publications

1. Laiali Almazaydeh, Khaled Elleithy, Miad Faezipour, Helen Ocbagabir, "SVM- Based Sleep Apnea Identification Using Optimal RR-Interval Features of ECG Signal," accepted (to appear) in special issue of IEEE Journal on Translational Engineering in Health and Medicine, 2013.
2. Wafa Elmannai, Abdul Razaque, Khaled Elleithy, "Deployment of TCP University of Bridgeport (UB) to Control Law Enforcement Department over Wireless Mesh Network," Journal of Communications and Computer Engineering, Volume 3, Issue 1, 2013, pp. 40-47.
3. Abdul Razaque and Khaled Elleithy, "Controlling Attacks of Rogue Dynamic Host Configuration Protocol (DHCP) to Improve Pedagogical Activities in Mobile Collaborative Learning (MCL) Environment, "International Journal of Communication and Computer Engineering, (In press).
4. Ali Elrashidi, Khaled Elleithy, and Hassan Bajwa, "Performance Analysis of a Microstrip Printed Antenna Conformed on Cylindrical Body at Resonance Frequency 4.6 GHz for TM01 Mode," Procedia Computer Science, Elsevier, Volume 10, 2012, Pages 775–784.
5. Munther H. Abualkibash and Khaled Elleithy, Cloud Computing: The Future of IT Industry," International Journal of Computer Networks & Communications (IJCNC), Vol.3, No.4, pp. 1 -12, July 2012.
6. Laiali Almazaydeh, Miad Faezipour, Khaled Elleithy, "A Neural Network System for Detection of Obstructive Sleep Apnea Through SpO2 Signal Features," International Journal of Advanced Computer Science and Applications (IJACSA), Vol. 3, No.5, pp. 7 – 11, June 2012.
7. Marwah Almasri, Khaled Elleithy, Abdul Razaque, "Analytical Study of Pre-Congestion Notification (PCN) Techniques," International Journal of Computer Networks & Communications, Vol.4, No.4, pp. 61 – 76, July 2012.
8. Wafa Elmannai, Abdul Razaque, Khaled Elleithy, "TCP-UB: A new Congestion Aware Transmisiosn Control Protocol Variant," International Journal of Computer Networks & Communications, Vol.4, No.4, pp. 129 – 141, July 2012..

9. Abdul Razaque and Khaled Elleithy, “Discovery of Malicious Attacks to Improve Mobile,” International Journal of Computer Networks & Communications, Vol.4, No.4, pp. 21 – 40, July 2012.
10. Delan Alsoufi, Khaled Elleithy, Tariq Abuzaghleh and Ahmad Nassar, “Security in Wireless Sensor Networks – Improving the Leap Protocol, International Journal of Computer Science & Engineering Survey (IJCSES) Vol.3, No.3, pp. 1 – 14, June 2012.
11. Ali Elrashedi, Khaled Elleithy, and Hassan Bajwa, “The Performance of a Cylindrical Microstrip Printed Antenna for TM10 Mode Using Two Different Substrates at 5 GHz Range,” International Journal of Materials Engineering, Accepted.
12. Ali Elrashedi, Khaled Elleithy, and Hassan Bajwa, “Effect of Temperature on the Performance of a Cylindrical Microstrip Printed Antenna for TM01 Mode Using Different Substrates,” Journal of Wireless Networking and Communications, Accepted.
13. Ali Elrashedi, Khaled Elleithy, and Hassan Bajwa, “Mutual Coupling Coefficient of a Microstrip Printed Antenna Array Mounted on Cylindrical Body for TM10 Mode at 5 GHz Range Using Three Different Substrate,” International Journal of Networks and Communications, vol. 1, pp. 12-18, June 2012.
14. Ali Elrashedi, Khaled Elleithy, and Hassan Bajwa, “Coupling Analysis of Microstrip Printed Antenna Array at 5 GHz Conformed on a Cylindrical Body for TM01 Mode,” Science and Technology, Accepted.
15. Ali Elrashedi, Khaled Elleithy, and Hassan Bajwa, “Return Loss and Mutual Coupling Coefficient of a Microstrip Printed Antenna Array Conformed on a Cylindrical Body for TM01 Mode,” Electrical and Electronic Engineering, vol. 1, pp. 6-11, June 2012.
16. Ali Elrashidi, Khaled Elleithy, and Hassan Bajwa, “Effect of Curvature on a Microstrip Printed Antenna Conformed on Cylindrical Body at Superhigh Frequencies,” International Journal of Instrumentation Science, vol.1, pp. 1-7, June 2012.
17. Ammar Odeh and Khaled Elleithy, “Steganography in Arabic Text Using Zero Width and Kashidha Letters,” International Journal of Computer Science & Information Technology (IJCSIT), Vol 4, No 3, pp. 1 – 11, June 2012.
18. Ali Elrashedi, Khaled Elleithy, and Hassan Bajwa, “The Effect of Curvature on the Return Loss and Mutual Coupling Coefficient of a Microstrip Printed Antenna Array Conformed on a Cylindrical Body for TM10 Mode,” International Journal of Electromagnetics and Applications, vol. 2, pp. 25-32, May 2012.
19. Abdul Razaque and Khaled Elleithy, "Multi-frame Signature-cum Anomaly-based Intrusion Detection Systems (MSAIDS) to Protect Privacy of Users over Mobile Collaborative Learning (MCL)”, Journal of E Technology, Volume 3, Number 2, May 2012.
20. Fadel Hussen, Khaled Elleithy, and Abdul Razaque,”Implementation of Fault Tolerance Algorithm to Restore Affected Nodes in Scheduling Clusters,” International Journal of Computer Networks & Communications (IJCNC) Vol.4, No.1, January 2012.
21. Khaled Elleithy and Abdul Razaque ,"Innovative usability testing to foster mobile applications to support collaborative learning environment (MCLE)".Journal of International Conference on Computer Science and Engineering (CSE-11) ICGST, 2012.
22. Abdul Razaque and Khaled Elleithy, ”Restoring the privacy and confidentiality of users over Mobile collaborative learning (MCL) environment," IEEE Transaction, Vol 9, LTL-2011.
23. Abdul Razaque and Khaled Elleithy, "Interactive linguistic prototypes to foster pedagogical activities through mobile collaborative learning (MCL)"International Journal of Interactive Mobile Technologies (iJIM)",Vol 5, No 3, 2011.
24. Abdul Razaque, Khaled Elleithy, Qassim Bani Hani, "Implementing and testing of anchor point node hybrid network architecture for remote areas with simulation of TCP”, IET Journal, Special Issue of Communication, 2011.
25. Ibrahim Alkore Alshalabi, “Effective M-Learning Design Strategies for Computer Sceince and Engineering Courses,” International Journal of Mobile Network Communications & Telematics (IJMNCT) Vol.2, No.1, February 2012.
26. Ahmad Nassar, Motaz Mohammed, Ali Elrashidi and Khaled Elleithy, “Virtual Wireless and Mobile Communication Laboratory,” Journal of Education, vol. 1, pp. 1-6, 2011.
27. Ali Elrashidi, Khaled Elleithy, and Hassan Bajwa, “The Fringing Field and Resonance Frequency of Cylindrical Microstrip Printed Antenna as a Function of Curvature,” International Journal of Wireless Communications and Networking (IJWCN), Dec. 2011.

28. Ali Elrashidi, Khaled Elleithy, and Hassan Bajwa, “Effect of Curvature on the Performance of a Microstrip Printed Antenna Conformed on Cylindrical Body Using Epsilam-10 Ceramic-Filled Teflon as a Substrate,” American Journal of Materials Science, pp. 8-17, Nov. 2011.
29. Ali Elrashidi, Khaled Elleithy, and Hassan Bajwa, “ Input Impedance, VSWR and Return Loss of a Conformal Microstrip Printed Antenna for TM10 mode Using Polymers as a Substrate Materials,” American Journal of Polymer Science, pp. 18-29, Nov. 2011.
30. Ali Elrashidi, Khaled Elleithy, and Hassan Bajwa, “Effect of Curvature on the Performance of Cylindrical Microstrip Printed Antenna for TM01 mode Using Two Different Substrates,” International Journal of Computer Science and Information Security (IJCSIS), Vol. 9, No.10, pp 8-16, Oct. 2011.
31. Ali Elrashidi, Khaled Elleithy, and Hassan Bajwa, “Input Impedance, VSWR and Return Loss of a Conformal Microstrip Printed Antenna for TM01 mode Using Two Different Substrates,” International Journal of Networks and Communications, pp. 1-9, Oct. 2011.
32. Ali Elrashidi, Khaled Elleithy, and Hassan Bajwa, “Performance Analysis of a Microstrip Printed Antenna Conformed on a Cylindrical Body for TM10 mode Using Two Different Substrates,” Electrical and Electronic Engineering Journal, pp. 25-32, Sep. 2011.
33. Ali Elrashidi, Khaled Elleithy, and Hassan Bajwa, “The Effect of Curvature on Resonance Frequency, Gain, Efficiency and Quality Factor of a Microstrip Printed Antenna Conformed on a Cylindrical Body for TM10 mode Using Different Substrates,” Science and Technology Journal, pp. 1-8, Sep. 2011.
34. Ali Elrashidi, Khaled Elleithy, and Hassan Bajwa, “Resonance Frequency, Gain, Efficiency and Quality Factor of a Microstrip Printed Antenna as a Function of Curvature for TM01 mode Using Different Substrates,” Journal of Wireless Networking and Communications, pp. 15-24, Aug. 2011.
35. Ali Elrashidi, Khaled Elleithy, and Hassan Bajwa, “The Performance of a Microstrip Printed Antenna Conformed on Cylindrical Body as a Function of Curvature at 4.6 GHz for TM01 Using RT/duroid-5880 PTFE as a Substrate,” International Journal of Electromagnetics and Applications, pp. 58-67, Jul. 2011.
36. Ali Elrashidi, Khaled Elleithy, and Hassan Bajwa, “Effect of Temperature on the Performance of a Cylindrical Microstrip Printed Antenna for TM01 Mode Using Different Substrates,” International Journal of Computer Networks & Communications (IJCNC), Jul. 2011.
37. Ali Elrashidi, Khaled Elleithy, and Hassan Bajwa, “The Performance of a Cylindrical Microstrip Printed Antenna for TM10 Mode as a Function of Temperature for Different Substrates,” International Journal of Next-Generation Networks (IJNGN), Jul. 2011.
38. T. Sobh, Khaled M. Elleithy, J. Lee, A. El- Elrashidi, J. Joy, and L. Manole, "Design and Implementation of Wireless Camera, Communication and Control Modules for a Transformable Unmanned Aerial Vehicle", the Journal of Intelligent and Robotic Systems, Springer, April 2011.
39. Khaled Elleithy and Varun Rao, “Femto Cells: Current Status and Future Directions,” International Journal of Next-Generation Networks (IJNGN), Vol. 3, No. 1, March 2011, pp. 1-9.
40. Manal Al- Al-bzoor and Khaled Elleithy, “WIMAX Basics from PHY Layer to Scheduling and Multicasting Approaches,” International Journal of Computer science and engineering Survey (IJCSES), Vol. 2, No. 1, February 2011, pp. 1-17.
41. Thabet Mismar, Khaled Elleithy, and Saikat Ray, “Energy Efficient Military Mobile Base Station,” International Journal of Wireless & Mobile Networks (IJWMN), Vol. 2, No. 4, November 2010.
42. Mohammed A. Abuhelaleh and Khaled M. Elleithy, “Security in Wireless Sensor Networks: Key Management Module in SOOAWSN,” International Journal of Network Security & Its Applications (IJNSA), Vol. 2, No. 4, October 2010.
43. Syed S. Rizvi, Aasia Riasat and Khaled M. Elleithy, "An Efficient Optimistic Time Management Algorithm for Discrete-Event Simulation System”, International Journal of Simulation Modeling, Vol. 9, No. 3: September 2010, pp 117-130.
44. Syed S. Rizvi, Khaled M. Elleithy, Aasia Riasat and Min Song, “A Low-Complexity Optimum Multiuser Receiver for DS-CDMA Wireless Systems,” Journal of Communications, Vol 5, No. 6, June 2010, pp. 455 – 466.
45. Anil Mathew, Nithin Chandrababu, Khaled Elleithy, and Syed Rizvi, “Interference of 802.11B WLAN and Bluetooth: Analysis and Performance Evaluation,” International Journal of Computer Networks (IJCN), Vol. 2, No. 3, May 2010.

46. Syed S. Rizvi, Aasia Riasat, and Khaled M. Elleithy. "Combining Private and Public Key Encryption Techniques for Providing Extreme Secure Environment for an Academic Institution Application, " International Journal of Network Security & Its Applications (IJNSA)-(AIRCC Publication), Vol.2, No.1, January 2010.
47. Syed S. Rizvi, Aasia Riasat, and Khaled M. Elleithy "Deterministic Formulization of End-to-End Delay for Multicast Systems" International Journal of Computer Networks & Communications (IJCNC), Vol. 2, No. 1, January 2010.
48. Syed S. Rizvi, Aasia Riasat, and Khaled M. Elleithy "Quantification of Capacity and Transmission Delay for Mobile Ad Hoc Networks (MANET)" International Journal of Communications, Network and System Sciences, Vol. 3, No. 1, 2010.
49. Syed S. Rizvi, Khaled M. Elleithy, and Aasia Riasat "An Analytical Model for Approximating the Bandwidth Efficiency for Multicast Systems" MASAUM Journal of Computing, Vol. 1, Issue. 4, 2009. (Invited paper).
50. Syed S. Rizvi, Khaled M. Elleithy, and Aasia Riasat "A Quantitative Analysis of Handover Time at MAC Layer for Wireless Mobile Networks" International Journal of Wireless & Mobile Networks, IJWMN, Vol. 1, No. 3, pp. 21 - 28, 2009.
51. Syed S. Rizvi, Khaled M. Elleithy, and Aasia Riasat "Deterministic Formulization of SNR for Wireless Multiuser DS-CDMA Networks" International Journal of Computer Science and Information Security (IJCSIS). Vol. 3, No. 1, pp. 25-34, 2009.
52. Syed S. Rizvi and Khaled M. Elleithy "A New Scheme for Minimizing Malicious Behavior of Mobile Nodes in Mobile Ad Hoc Networks. International Journal of Computer Science and Information Security (IJCSIS)" Vol. 3, No. 1, pp. 45-54, 2009.
53. Syed S. Rizvi, Aasia Riasat, and Khaled M. Elleithy "A Low-Complexity Algorithm for Improving Computing Power of Multiuser Receivers for Next Generation Wireless Systems" International Journal of Computer Science and Network Security (IJCSNS). Vol. 9, No. 6, pp. 1-10, 2009.
54. Mohammed A. Abuhelaleh, Khaled M. Elleithy, Thabet M. Mismar, "Clustered Hierarchy in Sensor Networks: Performance and Security", International journal of Network Security & Its Applications, IJNSA, Vol.1, No.2, PP. 51-62, 2009.
55. Sobh, Khaled M. Elleithy and S. Patel, "Reverse Engineering of VLSI Chips: A Roadmap," The Journal of Engineering and Applied Sciences, Volume 2, number 2, pp 290-298, 2007.
56. K. M. Elleithy, D. Blagovic, W. Cheng, and P. Sideleau "Denial Of Service Attack Techniques: Analysis, Implementation and Comparison", Journal of Systemics, Cybernetics and Informatics, Volume 3, Number 1, PP. 66-71, 2006.
57. K. M. Elleithy, D. Blagovic, W. Cheng, and P. Sideleau "Implementation and Comparison of Denial Of Service Attack Techniques", Journal of Systemics, Cybernetics and Informatics, Volume 3, Number 1, 2006.
58. K. M. Elleithy, "Formal Verification of Systems On Chip: Current & Future Directions," System on Chip for Realtime Systems, W. Badawy and G. Jullian, editors, Kluwewr Academic Publishing, Fall 2003.
59. Elleithy, K. and Sobh, Tarek M., "A Framework for Reverse Engineering VLSI Chips." In Intelligent Systems and Signal Processing, Elsevier Science, PP. 383-388, 2003.
60. K. M. Elleithy, A. Sing, T. Narayanan, N. Bhalla, L. Al-Rashid, BlueDArt: Radition Data Collection and Warning System," Journal of Internet Technology, Vol. 4, No.3, PP. 171-178, July 2003.
61. K. M. Elleithy, H. Ha, S. Karki, A. Kaulgud, U. Rana, "An Optimal Algorithm to the IMT2000 Power Control," Journal of Internet Technology, Vol. 4, No. 3, PP. 147-152, July 2003.
62. K. M. Elleithy and Rama C. Reddy, "Comparison of Firewalls Security and Performance Issues," Journal of Internet Technology, PP. 175-183, April 2002.
63. Sobh and Khaled M. Elleithy, "Internet-Based Robotics and Automation." In the International Journal of Robotics & Automation, special issue on Web-based Automation, volume 17, Number 3, 103-105, 2002.
64. K. M. Elleithy and A. Komaralingam, "Automation and Management of Mobile Content ," International Journal of Robotics and Automation, November 2002.
65. K. M. Elleithy, and A. A. Amin, "Mapping CIRCAL based Algorithms to Event Logic," The Journal of University of Kuwait, Topical issue No. 1, pp. 27-42, Dec. 1996.

66. S. M. Sait, K. M. Elleithy, and M. Hassan, “Formal Synthesis of VLSI Layouts from Algorithmic Specifications,” Computer Systems Science and Engineering, vol. 11, No. 2, pp. 67-81, March 1996.
67. K. M. Elleithy, and M. A. Bayoumi, “A Systolic Architecture for Modulo Multiplication,” IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, vol. 42, no. 11, pp. 725-729, Nov. 1995.
68. K. M. Elleithy and M. A. Bayoumi, “Fast and Flexible Architectures for RNS Arithmetic Decoding,” IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, vol. 39, no. 4, pp. 226-235, April 1992.
69. K. M. Elleithy, M. A. Bayoumi and L. M. Delcambre, “VLSI Implementation of A Systolic Database Machine for Relational Algebra and Hashing,” Integration the VLSI Journal, No. 11, pp. 169-190, Nov. 1991.
70. K. M. Elleithy and M. A. Bayoumi, “A $\Theta(1)$ Algorithm for modulo Addition,” IEEE Transactions on Circuits and Systems, vol. 37, no. 5, pp. 628-631, May 1990.
71. N. El-Derini, K. M. Elleithy, and E. G. Mohammed, “Implementation and Analysis of a Bilinear Multiplier,” Faculty of Engineering Research Journal, Alexandria University, Vol. 12, 1983.

Conference Publications

1. Laiali Almazaydeh, Khaled Elleithy and Miad Faezipour, “A Panoramic Study of Obstructive Sleep Apnea Detection Technologies,” 28th International Conference on Computers and Their Applications, CATA-2013, March 4-6, 2013, Honolulu, Hawaii, USA.
2. Ammar Odeh and Khaled Elleithy, "Steganography in Text by Merge ZWC and Space Character," 28th International Conference on Computers and Their Applications, CATA-2013, March 4-6, 2013, Honolulu, Hawaii, USA.
3. Abdul Razaque and Khaled Elleithy, "Efficient Search (RES) for One-Hop Destination over Wireless Sensor Networks," 28th International Conference on Computers and Their Applications, CATA-2013, March 4-6, 2013, Honolulu, Hawaii, USA.
4. Laiali Almazaydeh, Khaled Elleithy, Varun Pande, Miad Faezipour, “OSA Screening Test at the Patient’s Home,” IEEE EMBS Conference on Micro and Nanotechnology in Medicine (IEEE MNMC 2012), Hawaii, USA, December 3-7, 2012.
5. Abdul Razaque and Khaled Elleithy,”Ambient Intelligence Paradigm to support pedagogical activities over wireless sensor networks”, Ubiquitous Computing and Ambient Intelligence (UCAmI), Gasteiz , Spain, December3-5, 2012.
6. Ammar Odeh and Khaled Elleithy, “Steganography in Arabic Text Using Full Diacritics Text,” 25th International Conference on Computers and Their Applications in Industry and Engineering (CAINE-2012), New Orleans, Louisiana, USA, November 14 – 16, 2012.
7. Laiali Almazaydeh, Khaled Elleithy, Varun Pande and Miad Faezipour, “Development of OSA Event Detection Using Threshold Based Automatic Classification,” 25th International Conference on Computers and Their Applications in Industry and Engineering (CAINE-2012), New Orleans, Louisiana, USA, November 14 – 16, 2012.
8. Varun Pande, Wafa Elmannai, Khaled Elleithy, “Optimized Algorithm for Fire Detection over WSN using Micaz Motes,” 25th International Conference on Computers and Their Applications in Industry and Engineering (CAINE-2012), New Orleans, Louisiana, USA, November 14 – 16, 2012.
9. Wafa Elmannai, Khaled Elleithy, Varun Pande, “Efficient and Optical Character Recognition Algorithm for Signature Recognition,” 25th International Conference on Computers and Their Applications in Industry and Engineering (CAINE-2012), New Orleans, Louisiana, USA, November 14 – 16, 2012.
10. Varun Pande, Khaled Elleithy, Laiali Almazaydeh, “Parallel Processing for Multi Face Detection and Recognition,” 25th International Conference on Computers and Their Applications in Industry and Engineering (CAINE-2012), New Orleans, Louisiana, USA, November 14 – 16, 2012.
11. Laiali Almazaydeh, Khaled Elleithy, Miad Faezipour, Helen Ocbagabir, “SVM-Based Sleep Apnea Identification Using Optimal RR-Interval Features of ECG signal,” IEEE Healthcare Technology Conference: Translational Engineering in Health & Medicine (IEEE HIC 2012), Houston, Texas, USA, November 7-9, 2012.

12. Laiali Almazaydeh, Khaled Elleithy, Miad Faezipour, “Obstructive Sleep Apnea Detection Using SVM-Based Classification of ECG Signal Features,” 34th Annual International Conference of the IEEE Engineering in Medicine and Biology (IEEE EMBC’12), San Diego, California, USA, August 28 - September 1, 2012.
13. Ali Elrashidi, Khaled Elleithy, and Hassan Bajwa, “Performance Analysis of a Microstrip Printed Antenna Conformed on Cylindrical Body at Resonance Frequency 4.6 GHz for TM01 Mode,” The 9th International Conference on Mobile Web Information Systems, Niagara Falls, Ontario, Canada, August 27-29, 2012
14. Sumaya Abusaleh, Varun Pande, Khaled Elleithy, “Optimized Algorithm for Face Detection Integrating Different Illuminating Condition”, The 2012 International Conference on Image Processing, Computer Vision, and Pattern Recognition (IPCV’12), Las Vegas, Nevada, USA, July 16-19, 2012.
15. Abdul Razaque and Khaled Elleithy, “Supportive framework and innovative mobile application to augment mobile collaborative learning (MCL) activities ”In proceedings of IEEE International Conference on Education & E-Learning Innovations (ICEELI) July 1-3, 2012, Sousse, Tunisia.
16. Abdul Razaque and Khaled Elleithy, “ Mobile Supported Content Server (MSCS) to improve Mobile learning activities ”In proceedings of IEEE International Conference on Education & E-Learning Innovations (ICEELI) July 1-3, 2012, Sousse, Tunisia.
17. Laiali Almazaydeh, Khaled Elleithy, Miad Faezipour, "Detection of Obstructive Sleep Apnea Through ECG Signal Features," 2012 IEEE International Conference on Electro/Information Technology (IEEE eit2012), Indianapolis, Indiana, USA, May 6-8, 2012.
18. Ammar Odeh, Aladdin Alzubi, Qassim Bani Hani, Khaled Elleithy ”Steganography by Multipoint Arabic Letters, 2012 IEEE Long Island Systems, Applications and Technology Conference, Farmingdale State College - State University of New York, May 4, 2012.
19. Abdul Razaque, Khaled Elleithy, “Restoring privacy of users to foster Mobile Collaborative learning (MCL)”, ASEE Northeast Section Conference, University of Massachusetts Lowell, April 27th - 28th, 2012.
20. Wafa Elmannai, Khaled Elleithy and Abdul Razaque, “A High Performance and Efficient TCP Variant, ASEE Northeast Section Conference, University of Massachusetts Lowell, April 27th - 28th, 2012.
21. Ali Elrashidi, Khaled Elleithy, and Hassan Bajwa, “A New Analytical Performance Model for a Microstrip Printed Antenna,” ASEE Northeast Section Conference, University of Massachusetts Lowell, April 27th - 28th, 2012.
22. Ahmad Nassar, Motaz Mohammed, Ali M. Elrashidi and Khaled Elleithy, “Virtual Wireless and Mobile Communication Laboratory,” ASEE Northeast Section Conference, University of Massachusetts Lowell, April 27th - 28th, 2012.
23. Abdul Razaque, Khaled Elleithy “Detection of Attacks for Restoring Privacy of Users to improve Mobile Collaborative Learning (MCL) Over Heterogeneous Network” IEEE Fourth international conference on networked digital technology (NDT-2012), UAE, April 24-26, 2012.
24. Abdul Razaque and Khaled Elleithy,” Detection of Attacks for Restoring Privacy of Users to Improve Mobile Collaborative Learning (MCL) Over Heterogeneous Network”, In proceedings of Springer/ IEEE Networked Digital Technologies (NDT), Springer/IEEE, April 24-26, 2012, UAE.
25. Ali Elrashidi, Abdelrahman Elleithy, Majed Albogame and Khaled Elleithy, "Underwater Wireless Sensor Network Communication Using Electromagnetic Waves at Resonance Frequency 2.4 GHz,” 15th Communications and Networking Simulation (CNS'12) Symposium, Orlando, FL, USA, March 26 - 29, 2012.
26. Shalini Potham, Syed S. Rizvi, Khaled M. Elleithy, and Aasia Riasat, “A Use of Matrix with GVT Computation in Optimistic Time Warp Algorithm for Parallel Simulation, 15th Communications and Networking Simulation (CNS'12) Symposium, Orlando, FL, USA, March 26 - 29, 2012.
27. Abdul Razaque and Khaled Elleithy, "Novel framework for Mobile Collaborative Learning (MCL) to substantiate Pedagogical activities, "Internet Technologies & Society 2011 (ITS 2011), 8-11 December 2011, Beijing, China.
28. Abdul Razaque and Khaled Elleithy," "Innovative mobile application to foster Mobile Collaborative Learning Environment (MCLE)”, 10th International Information and Telecommunication Technologies Conference, Curitiba, Paraná State, Brazil, December 13-15, 2011.

29. Abdul Razaque and Khaled Elleithy "Architectured based Prototypes for Mobile Collaborative Learning to improve Pedagogical activities" Interactive collaborative learning (ICL), IEEE, Piešany, Slovakia, September 21 – 23, 2011.
30. Fathi Amsaad, Abdul Razaque, Khaled Elleithy, Anas Abu Munshar, Almobark Ethashmi Alarbi Moustafa, "Innovative Framework for Mobile Collaborative learning (MCL) Environment", IADIS, E-learning, Italy, July, 2011.
31. Abdul Razaque, N. Salama, Enam ul Haq, Abhilasha Tibrewal, Khaled Elleithy," Developing the Compiler to upgrade the features of Object Oriented Programming (OOP) C++", 2011 ASEE Northeast Section Conference, University of Hartford, Connecticut, April 29-30, 2011.
32. Abdul Razaque and Khaled Elleithy, "Design and Development of Hilological prototypes for Mobile Collaborative Learning (MCL)," 2011 ASEE Northeast Section Conference, University of Hartford, Connecticut, April 29-30, 2011.
33. Abdul Razaque, Khaled Elleithy, Fadel Hussen, Omar Etach, and Wafa Elmammai, "Novel Proactive Patch Peer Protocol to Support faster Delivery of Video-on-Demand," 2011 ASEE Northeast Section Conference, University of Hartford, Connecticut, April 29-30, 2011.
34. Wafa Elmammai, Abdul Razaque, and Khaled Elleithy "Simulation based Study of TCP Variants in Hybrid Network," 2011 ASEE Northeast Section Conference, University of Hartford, Connecticut, April 29-30, 2011.
35. Ali M Elrashidi, Khaled Elleithy, and Hassan Bajwa, "Conformal Microstrip Printed Antenna," 2011 ASEE Northeast Section Conference, University of Hartford, Connecticut, April 29-30, 2011.
36. Mohammed Abuhelaleh and Khaled Elleithy, "SOOAWSN: A complete framework for Wireless Sensor Networks," 2011 ASEE Northeast Section Conference, University of Hartford, Connecticut, April 29-30, 2011.
37. Mohammed Abuhelaleh and Khaled Elleithy, "Security in Wireless Sensor Networks: Key Intrusion Detection Module in SOOAWSN," 14th Communications and Networking Simulation (CNS'11) Symposium, Boston, April 4 – 7, 2011.
38. Eugene Gerety and Khaled Elleithy, "Location-Based Security for ID Document and ID Card Enrollment Stations," 14th Communications and Networking Simulation (CNS'11) Symposium, Boston, April 4 – 7, 2011.
39. Syed Rizvi and Khaled Elleithy, "A Generic Optimized Time Management Algorithms (OTMA), Framework for Simulating Large-Scale Overlay Networks," 14th Communications and Networking Simulation (CNS'11) Symposium, Boston, April 4 – 7, 2011.
40. Anil Mathew, Nithin Chandrababu, Khaled Elleithy, and Syed S. Rizvi, "IEEE 802.11 & Bluetooth Interference: Simulation and Coexistence," 7th Annual Conference on Communication Networks and Services Research (CNSR2009), Moncton, New Brunswick, Canada, May 11 - 13, 2009.
41. Hemen Patel, Syed S. Rizvi, and Khaled M. Elleithy, "A New Approach to Avoid Deadlock in Parallel Discrete Event Simulation (PDES) System, "2009 ASEE Northeast Section Conference, University of Bridgeport, Connecticut (2009 NE ASEE), April 3-4, 2009.
42. Sarosh Patel, Khaled Elleithy, and Syed S. Rizvi, "Hierarchically Segmented Routing (HSR) Protocol for MANET," 6th International Conference on Information Technology: New Generations ITNG 2009, April 27-29, 2009, Las Vegas, Nevada, USA, 2009.
43. Syed S. Rizvi, Khaled M. Elleithy and Aasia Riasat, "An Efficient Optimistic Time Management Algorithms for Discrete-Event Simulation System," 6th International Conference on Information Technology: New Generations ITNG 2009, April 27-29, 2009, Las Vegas, Nevada, USA, 2009.
44. Syed S. Rizvi, Mustafa A. Khan, Aasia Riasat, and Khaled M. Elleithy, "Combining Private and Public Key Encryption Techniques for Providing Extreme Secure Environment for an Academic Institution Application," 6th International Conference on Information Technology: New Generations ITNG 2009, April 27-29, 2009, Las Vegas, Nevada, USA, 2009.
45. Syed S. Rizvi, Mustafa Khan, Khaled M. Elleithy, and Aasia Riasat, "Formulization of SNR for Optimum Receiver in Synchronous DS-CDMA Systems," 6th International Conference on Information Technology: New Generations ITNG 2009, April 27-29, 2009, Las Vegas, Nevada, USA, 2009.
46. Syed S. Rizvi, Mustafa Khan, Khaled M. Elleithy, and Aasia Riasat, "Low-Complexity Algorithm for Improving the Computing Power of Multiuser Receivers for Next Generation Wireless Systems," 6th International Conference on Information Technology: New Generations ITNG 2009, April 27-29, 2009, Las Vegas, Nevada, USA, 2009.

47. Gowrilakshmi Ponuratinam, Bhumika Patel, Syed S. Rizvi, and Khaled M. Elleithy, “Improvement in the Spread Spectrum System in DSSS, FHSS, AND CDMA,” 2009 ASEE Northeast Section Conference, University of Bridgeport, Connecticut, April 3-4, 2009 Received Best Poster Paper Award).
48. Nilay Patel, Mayuresh Bhalekar, Pandya Nirav, Syed S. Rizvi, Khaled Elleithy, “Reducing Power Consumption in Sensor Network Using Sensor MAC Protocol,” 2009 ASEE Northeast Section Conference, University of Bridgeport, Connecticut, April 3-4, 2009.
49. Heven Patel, Hardik Patel, Aditya Jagannath, Syed S. Rizvi, Khaled Elleithy, "A Novel Simulation Based Methodology for the Congestion Control in ATM Networks," 2009 ASEE Northeast Section Conference, University of Bridgeport, Connecticut, April 3-4, 2009.
50. Gowrilakshmi Ponuratinam, Bhumika Patel, Syed S. Rizvi, and Khaled M. Elleithy, "Improvement in the Spread Spectrum System in DSSS, FHSS, AND CDMA," 2009 ASEE Northeast Section Conference, University of Bridgeport, Connecticut, April 3-4, 2009.
51. Muhammad Imran, Asif Shaikh, Syed S. Rizvi, Khaled M. Elleithy, "Performance Analysis of Switches in High Speed LAN," 2009 ASEE Northeast Section Conference, University of Bridgeport, Connecticut, April 3-4, 2009.
52. Mohamed A. Abuhelaleh, Khaled . M. Elleithy, and Thabet M. Mismar, “Routing in Sensor Networks: Performance and Security in Clustered Networks,” 2009 ASEE Northeast Section Conference, University of Bridgeport, Connecticut, April 3-4, 2009.
53. Syed Rizvi, Bevin Thomas, Khaled Elleithy, and Aasia Riasat, “A New Technique of Switch & Feedback Job Scheduling Mechanism in a Distributed System,“ 2009 Spring Simulation Multiconference (SpringSim'09), San Diego-Mission Valley; San Diego, CA, USA, March 22 - 27, 2009.
54. Bevin Thomas, Syed S. Rizvi, Khaled M. Elleithy, “Reducing Null Messages Using Grouping and Status Retrieval for a Conservative Discrete-Event Simulation System,” 2009 Spring Simulation Multiconference (SpringSim'09), San Diego-Mission Valley; San Diego, CA, USA, March 22 - 27, 2009.
55. Syed S. Rizvi, Hemen Patel, Khaled Elleithy, “Parallel and Distributed Algorithms and Implementations: Time Based Mutual Exclusion,“ 2009 Spring Simulation Multiconference (SpringSim'09), San Diego-Mission Valley; San Diego, CA, USA, March 22 - 27, 2009.
56. Syed Rizvi, Khaled Elleithy, and Aasia Riasat, “Analyzing SNR Performance of a Low-Complexity Wireless Multiuser Receiver for DS-CDMA Systems,” 2nd IEEE International Conference on Computer, Control & Communication (IEEE-IC4 2009), February 17-18, 2009, Karachi, Pakistan, 2009
57. Syed Rizvi, Khaled Elleithy, and Aasia Riasat, “A Low Complexity Optimum Multi-user Receiver for Maximizing the BER Performance for DS-CDMA Systems,” 2nd IEEE International Conference on Computer, Control & Communication (IEEE-IC4 2009), February 17-18, 2009, Karachi, Pakistan, 2009
58. Abdelrahman Elleithy, Syed S. Rizvi, and Khaled M. Elleithy, "Optimization and Job Scheduling in Heterogeneous Networks," International Joint Conferences on Computer, Information, and Systems Sciences, and Engineering (CIS2E 08), December 5 - 13, 2008.
59. Abdelrahman Elleithy, Syed S. Rizvi, and Khaled M. Elleithy, "Investigating the Effects of Trees and Butterfly Barriers on the Performance of Optimistic GVT Algorithm," International Joint Conferences on Computer, Information, and Systems Sciences, and Engineering (CIS2E 08), December 5 - 13, 2008.
60. Syed S. Rizvi, Majid A. Jafri, and Khaled Elleithy, "A Novel Optimization of the Distance Source Routing (DSR) Protocol for the Mobile Ad Hoc Networks (MANET)," International Joint Conferences on Computer, Information, and Systems Sciences, and Engineering (CIS2E 08), December 5 - 13, 2008.
61. Mohamed A. Abuhelaleh, Khaled . M. Elleithy, and Thabet M. Mismar “Modified LEACH – Energy Efficient Wireless Networks Communication,” International Joint Conferences on Computer, Information, and Systems Sciences, and Engineering (CIS2E 08), December 5 - 13, 2008..
62. Syed S. Rizvi, Shalini Poathan, and Khaled M. Elleithy, "A GVT Based Algorithm for Butterfly Barrier in Parallel and Distributed Systems," International Joint Conferences on Computer, Information, and Systems Sciences, and Engineering (CIS2E 08), December 5 - 13, 2008.

63. Syed S. Rizvi, Aasia Riasat, and Khaled M. Elleithy, "Performance Analysis of End System Multicast," 1st IEEE International Workshop on Future Multimedia Networking (IEEE FNM 2008), Cardiff, Wales, September 18, 2008.
64. Syed S. Rizvi, Aasia Riasat, and Khaled M. Elleithy, "A Mathematical Model for Evaluating the Performance of Multicast Systems," The 1st IEEE International Workshop on IP Multimedia Communications (IPMC 2008) August 4 - 7, 2008, St. Thomas U.S. Virgin Islands..
65. Jalpa Bani, Syed S. Rizvi, and Khaled M. Elleithy, "A Secured Rijndael Implementation to Withstand Cache Timing Attack Using Dynamic Cache Flushing (DCF) Algorithm," First IEEE International Workshop on Security of Computer Communications and Networks (SoCCaN 2008) August 4 - 7, 2008, St. Thomas U.S. Virgin Islands.
66. Syed S. Rizvi, Aasia Riasat, Khaled M. Elleithy, and Mustafa Khan, "The Worst and Best case Capacity Analysis of Mobile Ad Hoc Networks (MANET) using a 3-Phase Algorithm," The 5th Annual IEEE Communications Society Conference on Sensor, Mesh and Ad hoc Communications and Networks: IEEE SECON 2008. June 16-20, 2008, San Francisco Bay Area, 2008.
67. Syed S. Rizvi, Aasia Riasat, and Khaled M. Elleithy, "Reducing Null Message Traffic in Large Parallel and Distributed Systems," 13th IEEE Symposium on Computers and Communications (ISCC'08), July 6 - 9, 2008, Marrakech, Morocco. (Received Best Paper Award).
68. Syed S. Rizvi, Khaled M. Elleithy, and Aasia Riasat, "An Efficient Single Bit Store and Forward (SBSF) Routing Algorithm for Mesh-Hypercube (M-H) Networks," The 2008 World Congress in Computer Science, Computer Engineering, and Applied Computing WORLDCOMP'08. Las Vegas, Navada. pp. 69 - 75, July 14 - 17, 2008.
69. Syed S. Rizvi, Khaled M. Elleithy, and Aasia Riasat, "A Closed Form Expression for BER of Synchronous DS-CDMA Multi-user Detector," 9th IEEE Wireless Telecommunication Symposium (IEEE WTS 2008), Pomona, California, pp. 110-114, April 24-26 2008.
70. Auf Akhtar, Syed S. Rizvi, and Khaled M. Elleithy, "A Novel Approach of Using Data Guard for Disaster Recovery & Rolling Upgrades," ASEE Zone I Conference 2008, United State Military Academy, West Point, NY March 28 - 29.
71. Syed S. Rizvi, Khaled M. Elleithy, and Aasia Riasat, "SNR Analysis of a Low-Complexity Wireless Multiuser Receiver for DS-CDMA Systems," IEEE International Symposium on Wireless Pervasive Computing (IEEE ISWPC-08) 2008, 7 - 9 May 2008 Santorini, Greece.
72. Syed S. Rizvi, Khaled M. Elleithy, and Aasia Riasat, "Transformation Matrix System for Reducing the Computational Complexity of Wireless Multi-user Receivers for DS-CDMA Systems," 5th International Conference on Information Technology: New Generations ITNG 2008, April 7-9, 2008, Las Vegas, Nevada, USA.
73. Syed S. Rizvi, Khaled M. Elleithy, and Aasia Riasat, "A New Mathematical Model for Optimizing the Performance of Parallel and Discrete Event Simulation Systems," 11th Communications and Networking Simulation Symposium (CNS'08), Part of the 2008 Spring Simulation Multiconference (SpringSim'08). April 14-16, 2008. Crowne Plaza Ottawa, Ottawa, Ontario
74. Sarosh Patel and Khaled Elleithy, "Hierarchically Segmented Routing Protocol for MANETs". In the proceedings of the American Society for Engineering Education (ASEE 08) Zone I Conference, United States Military Academy, West Point, New York, USA, March 2008.
75. Abdelshakour Abuzneid, Khaled Elleithy, "Using Available Wireless / Wired Network Infrastructure for Public Safety and Emergency Early Response", The American Society for Engineering Education (Zone I), ASEE 2008, March 2008.
76. Syed S. Rizvi, Khaled M. Elleithy, and Aasia Riasat, "Deterministic Formalization of the Processing Gain for Reducing MAI in Wireless Multiuser DS-CDMA Systems," 5th Annual IEEE Consumer Communications and Networking Conference (IEEE CCNC'2008), pp. 859 - 860, Las Vegas, Nevada January 10 - 12, 2008.
77. Syed S. Rizvi, Khaled M. Elleithy, and Aasia Riasat, "Use of Processing Gain to Suppress Multi Access Interference (MAI) in CDMA Based Multiuser Receiver," International Wireless communications and Mobile Computing Conference 2007 (IWCMC 2007), Honolulu, Hawaii, August 12-16, 2007.
78. Syed S. Rizvi, Aasia Riasat, Muhammad S. Rashid, and Khaled M. Elleithy, "An Efficient Scheme for Traffic Management in ATM Networks," International Joint Conferences on Computer, Information, and Systems Sciences, and Engineering (CISSE 07), Bridgeport, CT, USA. December 3 - 12, 2007.

79. Syed S. Rizvi, Khaled M. Elleithy, and Aasia Riasat, “Trees and Butterflies Barriers in Distributed Simulation System: A Better Approach to Improve Latency and the Processor Idle Time”, IEEE International Conference on Information and Emerging Technologies (IEEE ICIET-2007), pp. 1 – 6, July 06-07, 2007, Karachi, Pakistan.
80. Syed S. Rizvi, Khaled M. Elleithy, and Aasia Riasat, “The Impact of Reduced Computational Complexity of Multiuser Detectors on the Processing Gain in a Wireless DS-CDMA Multiuser System,” Proceedings of the 2007 International Conference on Wireless Networks (ICWN’07). Part of the 2007 World Congress in Computer Science, Computer Engineering, & Applied Computing (Worldcomp’07), pp. 70 – 76, Las Vegas, Nevada, USA, June 25-28, 2007.
81. Syed S. Rizvi, K. M. Elleithy, and Aasia Riasat, “Transformation Matrix Algorithm for Reducing the Computational Complexity of Multiuser Receivers for DS-CDMA Wireless Systems,” Wireless Telecommunication Symposium (WTS 2007), Pomona, California, April 26-28 2007.
82. K. M. Elleithy and A. Maqbool, “Implementations of Location Awareness Technologies and their Applications” Wireless Telecommunication Symposium (WTS 2007), Pomona, California, April 26-28 2007.
83. Khushboo Patel, Chaitali Patel, Syed S. Rizvi, and Khaled M. Elleithy, "An Approach to Reduce the Energy Consumption in Wireless Sensor Networks through Active Nodes Optimization," 2007 Nefor Engineering Education Confernce, April 20-21, 2007.
84. Syed S. Rizvi, K. M. Elleithy, Aasia Riasat, “Minimizing the Null Message Exchange in Conservative Distributed Simulation,” International Joint Conferences on Computer, Information, and Systems Sciences, and Engineering, CISSE 2006, pp. 443-448 December 4-14, Bridgeport CT, 2006.
85. K. M. Elleithy and I. Rimawi, Design, Analysis and Implementation of a Cyber Vote System, International Joint Conferences, on Computer, Information, and Systems Sciences, and Engineering, (CIS2E 05), 10 - 20 December 2005, Virtual Conference.
86. K. M. Elleithy, D. Blagovic, W. Cheng, and P. Sideleau “Implementation and Comparison Of Denial Of Service Attack Techniques", the 8th World Multiconference on Systemics, Cybernetics and Informatics (SCI 2004), Orlando, USA, July 18-21, 2004.
87. K. M. Elleithy, S. Bell, B. Plaag, and D. Stone, “Implementation and Comparison of a Rules-Based Approach and a Statistical Approach Intrusion Detection Systems” I2TS'2003, 2nd International Information and Telecommunication, Technologies Symposium. Florianópolis, Brazil - Nov 26-29, 2003.
88. K. M. Elleithy and T. M. Sobh, “A Framework for Reverse Engineering VLSI Chips,” IFAC Conference on Intelligent Control and Signal Processing, Portugal, April 2003.
89. K. M. Elleithy, A. Adebola, S. Shrestha , and B. Bhattarai, “Support of a Banking System Using WAP,” The 6th World Multiconference on systemics, cybernetics, and Informatics, Orlando, USA, July 14-18, 2002.
90. K. M. Elleithy, H. Ha Hyungate, K. Amol, R. Utpal, “Simulation and Improvement of IMT2000 Power Control,” The 6th World Multiconference on systemics, cybernetics, and Informatics, Orlando, USA, July 14-18, 2002.
91. K. M. Elleithy, “"Formal Verification of Systems On Chip: Current & Future Directions," invited paper, 2002 International Workshop on System-on-Chip for Real-Time Applications, Banff, Canada, July 6-7, 2002.
92. K. M. Elleithy and A. Komaralingam, “A Mobile Content Manager,” International Conference on Industrial Electronics, Technology and Automation IETA2001, Cairo, Egypt, 19-21 December.
93. K. M. Elleithy and K. Al-Utaibi, “Performance Analysis of a Priority Traffic Shaper for ATM Networks,” Applied Telecommunication Symposium, Seattle, Washington, April 22 -26, 2001
94. K. M. Elleithy & A. Al-Suwaiyan, “Network Traffic Characterization for high-speed networks supporting multimedia," 34th Annual Simulation Symposium, Seattle, Washington, April 22-26, 2001.
95. K. M. Elleithy and M. I. Khan "Security in Internet Payment System and Queuing Network Analysis " International Conference on Advances in Infrastructure for Electronic Business, Science, and Education on the Internet, Rome, Italy, August 2000. (Invited paper)
96. K. M. Elleithy & Alaa A. Amin, “Area Estimation for DSP Algorithms," IEEE Workshop on SiGNAL PROCESSING SYSTEMS (SiPS) Design and Implementation, Lafayette, Louisiana, October 11-13, 2000.
97. K. M. Elleithy, Abd-El-Fattah, E.G., “A Genetic Algorithm for Register Allocation” 9th great Lake Symposium on VLSI, Michigan, March 1999.

98. K. M. Elleithy and E. G. AbdelFattah, "A Simulated Annealing Algorithm for Register Allocation," Fifth Saudi Engineering Conference, pp. 373-382, March 1999
99. M. A. Aref and K. M. Elleithy, “HOOVER: Hardware Object-Oriented Verification,” 8th Great Lake Symposium on VLSI, GLS’98, Lafayette, Louisiana, Feb. 1998.
100. K. M. Elleithy and E. G. Abd-El-Fattah, “New Non-deterministic Approaches for Register Allocation,” 4th IEEE International Conference on Electronics, Circuits, and Systems, Cairo, Dec. 1997.
101. K. M. Elleithy, and S. H. Abdul-Jauwad, “A Transputer Based Sonar Range finding,” The Journal of The Faculty of Science, United Arab Emirates University, May 1997, pp. 100-108.
102. A. Amin and K. M. Elleithy, “Self Timing of Event Logic Data-Paths,” The Eighth International Conference on Microelectronics, Cairo, Dec. 1996.
103. K. M. Elleithy and M. A. Aref, “Verification Strategy in Prover,” The Asia Pacific Conference on Circuits and Systems, Seoul, Korea, Nov. 18-21, 1996.
104. K. M. Elleithy, “Choosing System Moduli for RNS Arithmetic Processors,” 30th Annual Asilomar Conference on Signals, Systems, and Computers, Pacific Grove, California, Nov. 3-6, 1996.
105. Amin and K. M. Elleithy, “Mapping CIRCAL based Algorithms to Event Logic using a Standard Cell Library,” The Seventh International Conference on Microelectronics, Kuala Lumpur, Dec. 19-21, 1995.
106. K. M. Elleithy and A. A. Amin, “Synthesizing Digital Signal Processing Algorithms from Formal Descriptions,” The Sixth International Conference on Signal Processing Applications and Technology, Boston, pp. 901-905, Oct. 24-26, 1995.
107. K. M. Elleithy and A. A. Amin, “An Event Logic Architecture for CIRCAL Algorithms,” The Seventh IASTED International Conference on Parallel and Distributed Computing and Systems, Washington, D. C., pp. 311-314, Oct. 18-21, 1995.
108. K. M. Elleithy and A. A. Amin, “A Formal Approach for Synthesizing Digital Signal Processing Algorithms,” IEEE Singapore International Conference on Signal Processing, Circuits and Systems, Singapore, pp. 163-168, July 3-7, 1995.
109. K. M. Elleithy, “An Integer Programming Approach for Choosing the System Moduli for RNS Processor,” Minisymposium on Optimization Theory and Applications, KFUPM, Dhahran, May 30, 1995.
110. K. M. Elleithy, “Formal Verification of DSP VLSI Architectures,” International Conference on Electronics, Circuits and Systems, Cairo, Egypt, Dec. 19-22, 1994.
111. K. M. Elleithy and M. A. Al-Humaigani, “Formal Environments for Parallel Hardware Description Languages,” International Conference on Electronics, Circuits and Systems, Cairo, Egypt, pp. 440-445, Dec. 19-22, 1994.
112. K. M. Elleithy and M. A. Al-Humaigani, “Parallelism Analysis and Extraction of Digital Signal Processing Algorithms,” Twenty-Eighth Annual Asilomar Conference on Signals, Systems and Computers, Pacific Grove, California, pp. 1041-1045, Oct. 31-Nov. 2, 1994.
113. Maruf and K. M. Elleithy, “RISC Support for Real-Time Features,” Sixth International Conference on Microelectronics, Istanbul, Turkey, Sep. 5-7, 1994.
114. K. M. Elleithy and M. A. Al-Humaigani, “Formal Verification of DSP VLSI Architectures: A Tutorial” 37th Midwest Symposium on Circuits and Systems, Lafayette, Louisiana, pp. 351-355, August 3-5, 1994.
115. K. M. Elleithy and M. A. Aref, “A Rule-based Approach for High Speed Adders Design Verification,” 37th Midwest Symposium on Circuits and Systems, Lafayette, Louisiana, pp. 274-277, August 3-5, 1994.
116. K. M. Elleithy, Samir H. Abdul-Jauwad, and Jafar Al-Rashed, “A Transputer Based Sonar Range finding,” Workshop on Optimization and Parallel Computation, Al-Ain, United Arab Emirates, May 8-11, 1994.
117. K. M. Elleithy and Muhammed A. Al-Humaigani, “A Characteristic Model for Formal Parallel Hardware Description Languages,” Workshop on Optimization and Parallel Computation, Al-Ain, United Arab Emirates, May 8-11, 1994.
118. S. M. Sait, M. Hasan, and K. M. Elleithy, “Design of a cell Library for Formal High Level Synthesis,” Mediterranean Electrontecnical Conference Melecon 94, April 1994.
119. K. M. Elleithy, Sadiq M. Sait and Masud-ul-Hasaan, “Formal Design of VLSI Systems,” Fifth International Conference on Microelectronics, Dhahran, pp. 214-219, Dec. 1993.

120. K. M. Elleithy, “Formal Hardware Verification of VLSI Architecture Current Status and Future Directions,” Fifth International Conference on Microelectronics, Dhahran, pp. 197-201, Dec. 1993.
121. M. A. Aref and K. M. Elleithy, “PROVER: A Production System for Formal Hardware Verification,” Fifth International Conference on Microelectronics, Dhahran, pp. 210-213, Dec. 1993.
122. K. M. Elleithy and Mostafa A. Aref, “A Production Based System for Formal Verification of Digital Signal Processing Architectures,” Twenty-Seventh Annual Asilomar Conference on Signals, Systems and Computers, Pacific Grove, California, pp. 1618-1622, Nov. 1-3, 1993.
123. K. M. Elleithy and Adel Al-Massarani, “PLORA: A Prolog and LISP oriented RISC Architecture,” 18th International Conference for Statistics and Computer Science and its Scientific and Social Applications, Egypt, April 1993.
124. K. M. Elleithy, “Systolic Arithmetic Architectures,” Fifth International Conference on VLSI Design, Bangalore, India, January 1992.
125. K. M. Elleithy and M. A. Bayoumi, “A Massively Parallel RNS Architecture,” Twenty Fifth Annual Asilomar Conference on Signals, Systems and Computers, Pacific Grove, California, pp. 408-412, November 1991.
126. K. M. Elleithy and M. A. Bayoumi, “Formal Design of RNS Processors,” Proc. of the International Conference on Circuits and Systems, pp. 605-608, China, June 1991.
127. K. M. Elleithy and M. A. Bayoumi, “From Algorithms to Parallel architectures: A Formal Approach,” Proc. of Fifth International Symposium on Parallel Processing, pp. 358-363, April 1991.
128. K. M. Elleithy and M. A. Bayoumi, “Formalization of DSP Architectural Synthesis,” Proc. of the Fifth European Signal Processing Conference, Spain, pp. 1451-1454, vol. 3., Sep. 1990.
129. K. M. Elleithy and M. A. Bayoumi, “A Formal Design Methodology for Parallel Architectures,” Proc. of the International Conference on Application Specific Array Processors, pp. 603-614, Sep. 1990.
130. K. M. Elleithy and M. A. Bayoumi, “Formal Synthesis of Parallel Architectures from Recursive Equations,” Proc. of the 1990 International Conference on Parallel Processing, vol. I, pp. 145-148, Aug. 1990.
131. K. M. Elleithy and M. A. Bayoumi, “Synthesizing DSP Architectures from Behavioral Specifications: A Formal Approach,” Proc. of the 1990 IEEE International Symposium for Circuits and Systems, pp. 1131-1134, May 1990.
132. K. M. Elleithy and M. A. Bayoumi, “A Formal High Level Synthesis Approach for DSP Architectures,” Proc. of the 1990 International Conf. on Acoustics, Speech and Signal Processing, pp. 897-900, April 1990.
133. K. M. Elleithy and M. A. Bayoumi, “A Formal Framework for Synthesis of Parallel Architectures,” Proc. of the Fourth Annual Symposium on Parallel Processing, April 1990.
134. K. M. Elleithy and M. A. Bayoumi, “A Framework for High Level Synthesis of Digital Architectures from U-recursive Algorithms,” Proc. of the ACM Eighteenth Annual Computer Science Conference, pp. 305-311, Feb. 1990.
135. K. M. Elleithy, M. A. Bayoumi, and K. P. Lee, “$\Theta(\log n)$ Architectures for RNS Arithmetic Decoding,” Proc. of the 9th Symposium on Computer Arithmetic, pp. 202-209, Sep. 1989.
136. K. M. Elleithy and M. A. Bayoumi, “A $\Theta(\log n)$ Algorithm for Modulo Multiplication,” Proc. of the 32nd Midwest Symposium on Circuits and Systems, pp. 353-356, Aug. 1989.
137. P. Lee, M. A. Bayoumi and K. M. Elleithy, “A Fast and Flexible Residue Decoder Based on the Chinese Remainder Theorem,” Proc. of The 1989 International Symposium on Circuits and Systems, pp. 200-203, May 1989.
138. K. M. Elleithy, M. A. Bayoumi, and L. M. Delcambre, “A Systolic Machine for Relational Database and Hashing,” Proc. Third Annual Parallel Processing Symposium, pp. 621-635, Mar. 1989.
139. M. A. Bayoumi and K. M. Elleithy, “A Logic Programming Approach for DSP architectures Design,” Proc. of the 22nd IEEE Asilomar Conf. on Signals, Systems and Computers, vol. 2, pp. 753-757, Nov. 1988.

Presentations in Conferences, Meetings, and Seminars

1. "Underwater Wireless Sensor Network Communication Using Electromagnetic Waves at Resonance Frequency 2.4 GHz," 15th Communications and Networking Simulation (CNS'12) Symposium, Orlando, FL, USA, March 26 - 29, 2012.
2. "A Use of Matrix with GVT Computation in Optimistic Time Warp Algorithm for Parallel Simulation, 15th Communications and Networking Simulation (CNS'12) Symposium, Orlando, FL, USA, March 26 - 29, 2012.
3. "Security in Wireless Sensor Networks: Key Intrusion Detection Module in SOOAWSN," 14th Communications and Networking Simulation (CNS'11) Symposium, Boston, April 4 – 7, 2011.
4. "A Generic Optimized Time Management Algorithms (OTMA), Framework for Simulating Large-Scale Overlay Networks," 14th Communications and Networking Simulation (CNS'11) Symposium, Boston, April 4 – 7, 2011.
5. "Transformation Matrix Algorithm for Reducing the Computational Complexity of Multiuser Receivers for DS-CDMA Wireless Systems," Wireless Telecommunication Symposium (WTS 2007), Pomona, California, April 26-28, 2007.
6. "Implementations of Location Awareness Technologies and their Applications" Wireless Telecommunication Symposium (WTS 2007), Pomona, California, April 26-28, 2007.
7. "Implementation and Comparison Of Denial Of Service Attack Techniques", the 8th World Multiconference on Systemics, Cybernetics and Informatics (SCI 2004), Orlando, USA, July 18-21, 2004.
8. "A Framework for Reverse Engineering VLSI Chips," IFAC Conference on Intelligent Control and Signal Processing, Portugal, April 2003.
9. "Support of a Banking System Using WAP," The 6th World Multiconference on systemics, cybernetics, and Informatics, Orlando, USA, July 14-18, 2002.
10. "Simulation and Improvement of IMT2000 Power Control," The 6th World Multiconference on systemics, cybernetics, and Informatics, Orlando, USA, July 14-18, 2002.
11. "E-commerce" Public Lecture organized by Royal Commission at Jubail, Saudi Arabia, Dec. 1999.
12. " Area Estimation for DSP Algorithms," IEEE Workshop on SiGNAL PROCESSING SYSTEMS (SiPS) Design and Implementation, Lafayette, Louisiana, October 11-13, 2000.
13. "E-commerce" Saudi Internet 98 Meeting, Riyadh, Dec. 5-7, 1999
14. "A Genetic Algorithm for Register Allocation" 9th great lake Symposium on VLSI, Michigan, March 1999.
15. "A Simulated Annealing Algorithm for Register Allocation," Fifth Saudi Engineering Conference, March 1999
16. "Net Security" Information Technology Center Internal Seminar for ITC Staff, Dhahran, Saudi Arabia, Nov. 1998.
17. "Internet/Intranet for Business" Dhahran 15th Annual Computer, Communication & Office Technology Exhibition, Oct. 17-22, 1998.
18. "HOOVER: Hardware Object-Oriented Verification," 8th Great Lake Symposium on VLSI, GLS'98, Lafayette, Louisiana, Feb. 1998.
19. "New Non-deterministic Approaches for Register Allocation," 4th IEEE International Conference on Electronics, Circuits, and Systems, Cairo, Dec. 1997.
20. "HOOVER: Hardware Object-Oriented Verification," 8th Great Lake Symposium on VLSI, GLS'98, Lafayette, Louisiana, Feb. 1998.
21. "Electronic Commerce," Annual Computer Exhibition, KFUPM, April 1997.
22. "Formal Hardware Verification," ICS/COE Fall Seminar Series, KFUPM, Dec. 1996.
23. "Choosing System Moduli in RNS," ICS/COE Fall Seminar Series, Nov. 1996.
24. "Choosing System Moduli for RNS Arithmetic Processors," 30th Annual Asilomar Conference on Signals, Systems, and Computers, Pacific Grove, California, Nov. 3-6, 1996.
25. "Synthesis of Digital Signal Processing Algorithms from Formal Descriptions," The Sixth International Conference on Signal Processing Applications and Technology, Boston, Oct. 24-26, 1995.
26. "An Event Logic Architecture for CIRCAL Algorithms," The Seventh IASTED International Conference on Parallel and Distributed Computing and Systems, Washington, D. C., Oct. 18-21, 1995.

27. “An Integer Programming Approach for Choosing the System Moduli for RNS Processor,” Minisymposium on Optimization Theory and Applications, KFUPM, Dhahran, May 30, 1995.
28. “Formal Verification of DSP VLSI Architectures,” International Conference on Electronics, Circuits and Systems, Cairo, Egypt, Dec. 19-22, 1994.
29. “Formal Environments for Parallel Hardware Description Languages,” International Conference on Electronics, Circuits and Systems, Cairo, Egypt, Dec. 19-22, 1994.
30. “Formal Verification of DSP VLSI Architectures: A Tutorial” 37th Midwest Symposium on Circuits and Systems, Lafayette, Louisiana, August 3-5, 1994.
31. “A Rule-based Approach for High Speed Adders Design Verification,” 37th Midwest Symposium on Circuits and Systems, Lafayette, Louisiana, August 3-5, 1994.
32. “A Transputer Based Sonar Range finding,” Workshop on Optimization and Parallel Computation, Al-Ain, United Arab Emirates, May 8-11, 1994.
33. “A Characteristic Model for Formal Parallel Hardware Description Languages,” Workshop on Optimization and Parallel Computation, Al-Ain, United Arab Emirates, May 8-11, 1994.
34. “Formal Hardware Verification of VLSI Architecture Current Status and Future Directions,” Fifth International Conference on Microelectronics, Dhahran, Saudi Arabia, Dec. 1993.
35. “A Production Based System for Formal Verification of Digital Signal Processing Architectures,” Twenty-Seventh Annual Asilomar Conference on Signals, Systems and Computers, Pacific Grove, California, Nov. 1993.
36. “PLORA: A Prolog and LISP oriented RISC Architecture,” EinShams University, Cairo, April 1993.
37. “RISC Architectures,” Information & Computer Science Department Spring Lecture Series, King Fahd University, May 1992.
38. “Foundations of Hardware Design Correctness,” Computer Engineering Department, King Fahd University, December 1991.
39. “A Massively Parallel RNS Architecture,” 25th Asilomar Conference on Circuit and Systems, Pacific Grove, California, Nov. 1991.
40. “Formal High Level Synthesis,” Computer Engineering Department, King Fahd University, December 1990.
41. “Synthesizing DSP Architectures from Behavioral Specifications: A Formal Approach,” 1990 IEEE International Symposium for Circuits and Systems, New Orleans, May 1990.
42. “Formalizing Behavioral Specifications Synthesis,” Louisiana State University, Louisiana, April 1990.
43. “A Framework for High Level Synthesis of Digital Architectures from U-recursive Algorithms,” ACM Eighteenth Annual Computer Science Conference, Washington D. C., Feb. 1990.
44. “Efficient Implementations of the Chinese Remainder Theorem,” Computer Science and Automatic Control Department, Alexandria University, July 1989.
45. “A Systolic Machine for Relational Database and Hashing,” Third Annual Parallel Processing Symposium, Los Angles, Mar. 1989.
46. “Residue Arithmetic: Parallelism at the Algorithmic Level,” The Center for Advanced Computer Studies, University of Southwestern Louisiana, November 1987.

Honors

“Distinguished Professor of the Year”, University of Bridgeport, academic year 2006-2007.

Nominated for University of Bridgeport Distinguished Professor for academic year 2005 – 2006. Prof. Elleithy was one of two finalists.

Nominated for University of Bridgeport Distinguished Professor for academic year 2004 – 2005. Prof. Elleithy was one of two finalists.

Nominated for King Fahd University of Petroleum and Minerals for Best Teacher Award.

“Distinguished” Annual evaluation by KFUPM for 10 years in research, teaching and community services.

Honored at the “Academic Honors Day” of the University of South Western Louisiana for superior scholastic achievement, April 1988.

Date	Apr. 85
Awarder	Air Defense College, Alexandria, Egypt.
Award	Certificate of merit
Reason	Work in the college since Jan. 84

Date	June 83
Awarder	Egyptian Engineering Syndicate
Award	Badge of honor and Decoration
Reason	Graduation with the highest grade “Distinction with Degree of Honor”

Date	Apr. 82
Awarder	Alexandria University, Egypt
Award	Badge of honor and Decoration
Reason	Model student of Alexandria University for academic year 81/82

Period	Oct. 78 - June 83
Awarder	Alexandria University, Egypt
Scholarship	L.E. 120/year (Highest Scholarship in the university)
Reason	“Distinction” grade during all academic years

Date	June 1978
Awarder	Alexandria Governor, Egypt
Award	Badge of honor and Decoration
Reason	Scoring 96.25% in National High School Exam and nationally ranked fourth